

Attorney Docket No. 10559-270001 Serial No.: 09/675,816 Amendment dated November 10, 2003 Reply to Office Action dated September 9, 2003

REMARKS

Reconsideration and allowance of the above-referenced application are respectfully requested.

The title stands objected to as not being descriptive. In response, the title is changed herewith to "Digital Signal Processor With Multiple Instruction Destinations For A Pipelined System". However, should the examiner have any suggestions for a new and/or more descriptive title, these suggestions would be appreciated.

Claims 1-18 stand rejected under 35 USC 102 as allegedly being anticipated by Shiell. In response, all of the claims are amended to emphasize their patentable distinctions.

Specifically, all of the claims have been amended to recite that a single instruction can have multiple destinations, at least one of which requires encoded instruction and another of which requires decoded instruction information. This is completely different than Shiell. Shiell describes, at the top of column 5, that a number of different units are provided to decode the instructions in parallel. However, nowhere is there any teaching or suggestion of a single instruction being sent to multiple locations, part of which is decoded and another part of which is encoded. Therefore, these amendments completely

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patentably distinguish over the cited prior art, which does not teach or suggest this feature.

Claim 8 goes even a step further, by describing the first, second and third functional units, one of which requires an instruction, one of which requires a decoded portion of an instruction and one of which requires a re-encoded instruction. Once again, this is in no way taught or suggested by the cited prior art.

In view of the above amendments and remarks, therefore, all of the claims should be in condition for allowance. A formal notice to that effect is respectfully solicited.

Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

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